

PC_EYE/MONO

PCI Express Frame Grabber for 4 Multiplexed Analog Cameras

- PCI Express performance
- Industrial Cameras Supported
- With 40-MHz ADCs

→ preliminary

III Main Features

- PCI Express Frame Grabber Board
- Frame grabber for 8 multiplexed monochrome channels
- Trigger input
- On-board sync generator
- Restart cameras supported
- Real-time acquisition of images or image sequences directly into main memory
- Image memory format: monochrome with 8 bits/pixel
- PCI Express (x1) interface

III Technical Details

The PC_EYE/MONO frame grabber is intended for monochrome cameras. It supports industrial analog cameras with reset/restart and external trigger applications.

The PCI Express bus interface offers twice the transfer speed of the older PCI bus.

The region of interest, i.e. the part of the video information that is acquired, can be defined on a pixel basis for all video inputs with the same values - not always the full frame has to be acquired.

Pixel Clock Generation

The clock used to digitize all four channels is generated either by an on-board PLL, synchronized to the horizontal camera sync with a pixel jitter of ≤ 7 ns or it is taken directly from the pixel clock input for camera-synchronous digitization.

Analog-Digital Converter / Multiplexer

The A/D converter digitizes data with 8 bits resolution at rates between 5 and 40 MHz. The ADC can sample data from 8 input ports in a multiplexed fashion.

Trigger Processing

The PC_EYE/MONO has an opto-isolated trigger input with an additional output used to trigger cameras. Strobe lights can also be triggered by the trigger inputs.

DMA Controller

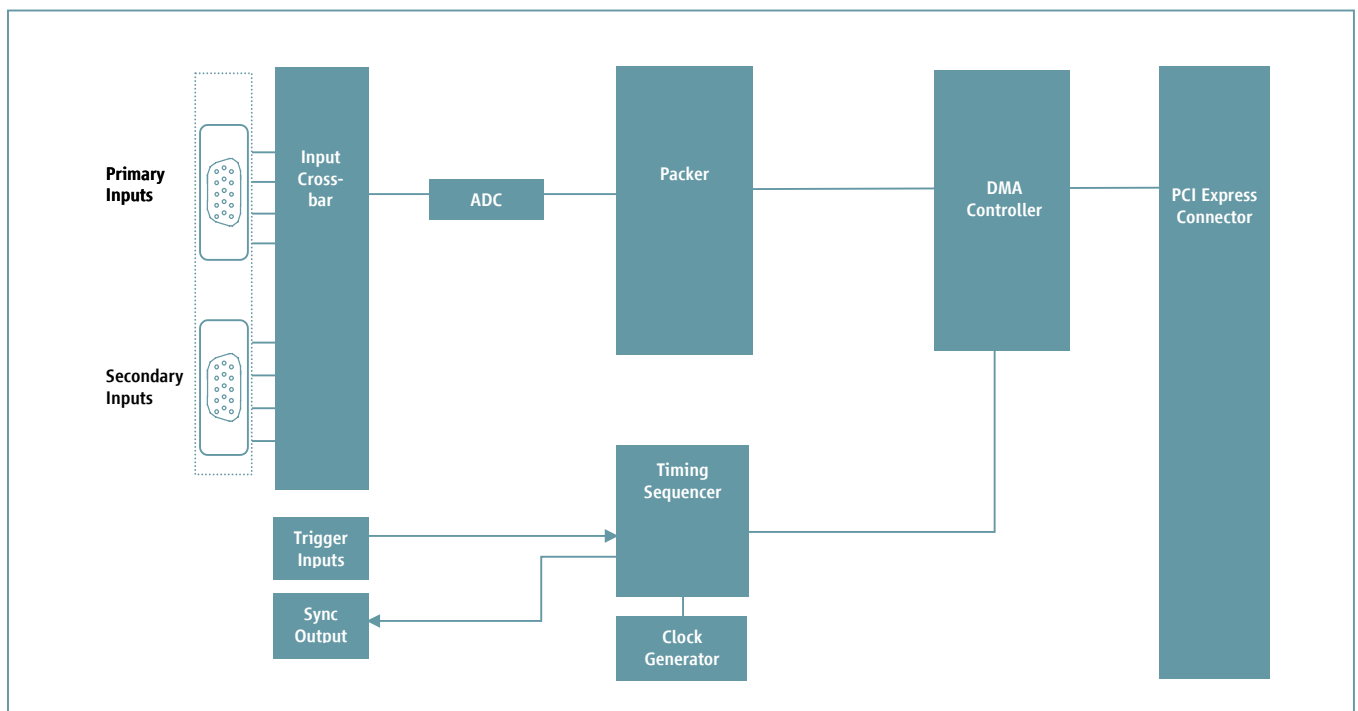
In the last step image data is transmitted by DMA directly into main memory or into the graphics card.

Bus	PCI Express
width	(1+1) bits in+out
speed	2500 MHz
Usable bandwidth	160 MB/s (typ.)

The PCI Express (x1) single-lane interface offers transfer rates of theoretical up to 250 MB/s in full duplex. Due to the PCI Express architecture with point-to-point connections instead of a common bus, each PCI Express card can utilize the full bandwidth.

Frame Grabber Basic Tools

Drivers for Windows come in the form of a DLL for Windows XP/Windows 7. Setup of the ADC, offset/gain, region-of-interest adjustment, camera selection, and camera setup files are supported. Permanent (live), single-shot, and



double-buffered acquisition of images can be requested and the status of the acquisition (active / finished) can be inquired.

Memory allocation for image buffers is also handled by the DLL at runtime; frame buffers appear in linear memory for easy addressing.

A setup program supports test and configuration of the board under Windows. Display routines using the DirectDraw standard are supplied in source. This software level is intended for users who already have their own software support available, such as image processing libraries, or wish to create application programs by themselves.

Frame Grabber Basic Tools is available for Windows 7/XP and for Linux. For the real-time operating systems VxWorks and OS-9 there is a reduced version (available on request), where parameters are set in configuration files only and where no display routines are included.

Cameras Supported

Cameras with reset/restart features are supported, as well as pixel clock input to the frame grabber for sub-pixel accuracy.

Cameras supported are:

Generic (Free-running), Generic CCIR625 (Free-running), Generic EIA (Free-running), Sony XC-55 (Full frame, Interlaced, Restart, Long time exposure, E-Donpisha II), Sony 003 P (Restart), Sony XC-ST50CE (t.b.d.), Sony XC-ST70CE (t.b.d., Full frame, Donpisha), Sony XC 8500 (Dual, Restart), Teli 3910 (Full frame, fixed, RTS pulsed Megapixel), JAI M-10 RS (Dual, Full frame, Restart), JAI M50 (Restart), JAI M70 (Full frame, Restart), JAI M1 (Free-running), JAI A11 (Full frame, Restart), JAI A50/A60 (Interlaced, Restart)

High-Level Software

Support for several 3rd-party imaging tools is available under Windows:

AdOculus (The Imaging Source) is a tool for visual image processing algorithm development. All of the basic algorithms are supplied in source form for easy modification and expansion.

Heurisko (Aeon Verlag+Studio) is a tool for development of highly optimized imaging algorithms using a C-like scripting language.

Halcon (MVTec) is a very complete tool set with an integrated scripting language.

The freeware imaging library IPL98 has been tested to work; there is a demo source, showing how to use it.

DB15	
Pin	Signal
1	Video04 / Trig 2+
2	Video05 / Trig 2-
3	Video06 / Trig 3-
4	Video07 / Trig 3-
5	Gnd
6	Gnd
7	Gnd
8	Gnd
9	Pout04 / Trig 1+
10	Gnd
11	Pout11 / Trig 1-
12	Clock
13	Pout03 / Trig 0+
14	Pout04 / Trig 0-
15	+12V

Note: Depend on jumper settings

GERMANY**FRANCE****USA****UK****ELTEC Elektronik AG**

Galileo-Galilei-Strasse 11
55129 Mainz
PO Box 10 03 64
55134 Mainz

Fon +49 6131 918 100
Fax +49 6131 918 195
Email info@eltec.com
www eltec.com

ELTEC International SARL

1, Allée des Garays
91872 Palaiseau
France

Fon +33 1 64 47 18 77
Fax +33 1 64 47 09 33
Email info.fr@eltec-france.fr
www eltec-france.fr

American ELTEC, Inc.

2401 Windjammer Way
Las Vegas, Nevada 89107
USA

Fon +1 702 878 40 85
Fax +1 702 878 47 35
Email info.us@eltec.com
www americaneltec.com

ELTEC International PLC

Unit 32, Stratford Office Village
Wolverton Mill
Milton Keynes MK12 5NS
United Kingdom

Fon +44 1908 32 00 55
Fax +44 1908 31 01 07
Email info.uk@eltec.com
www eltec.com

III Specifications

ADC

→ 8-bit, 40 MHz

Connectors

- 2 * 12V-camera supplies
- 8 * video input on 2 * DB-15
- 2 * sync/restart outputs (75 Ohm)
- 2 * Pixel clock input (TTL, 75 Ohm)
- 2 * Trigger input (TTL)

Environmental Conditions

- Storage Temperature: -20 °C - 70 °C
- Operating Temperature: 0 °C - 45 °C (2 m/s forced air cooling)
- Maximum Operating Humidity: 85 % relative

Power Requirements

- 0.55A max., 0.5A typ. at + 3.3 VDC ± 5 %
- 0.5A max., 0.3A typ. at + 12 VDC ± 5 %
- 1A max. total, 12V for camera supply (voltage supplied through on-board power-supply connector [floppy-type])
- 1.1A max. for each of the 2 camera supply outputs

PCI bus Requirements

- PCI Express (x1) compliant (Rev 1.0a)

MTBF

- T.b.d. hrs (computed after MIL-HDBK-217E)

Documentation

- Free Internet

Please contact your local sales office for detailed information.

